

DESCRIPTION

The A431L is a three-terminal Adjustable Shunt Voltage Reference. Output can be set to any value between VREF (1.24V) and 18V with two external resistors (see Figure 2).

The A431L operate from a lower voltage (1.24V) than the widely use A431, A431A and A432A shunt-regulator reference.

The A431L has a typical output impedance of 0.5 Ω . Active output circuitry provides a very sharp turn-on characteristic, making A431L excellent replacements for Zener diodes in many applications, including on-board regulation and adjustable power supplies.

The A431L is available in SOT-23 package.

ORDERING INFORMATION

Package Type	Part Number				
SOT-23	Го	A431LE3R-X			
SPQ: 3,000pcs/Reel	E3	A431LE3VR-X			
Note	X: Output Voltage Tolerance:				
	A, B or C				
	A=0.5%, B=1%, C=1.5%				
	R: Tape & Reel				
	V: Halogen free Package				
AiT provides all RoHS products					

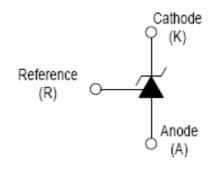
FEATURES

- Low Voltage Operation: VREF=1.24V
- Adjustable Output Voltage: VREF to 18V
- Reference Voltage Tolerances
 - -0.5% for A431L-A
 - -1% for A431L-B
 - -1.5% for A431L-C
- Low dynamic output impedance: 0.5Ω
- Low output noise voltage
- Fast on -state response
- Sink current capability of 0.1mA to100mA
- Available in SOT-23 package.

APPLICATION

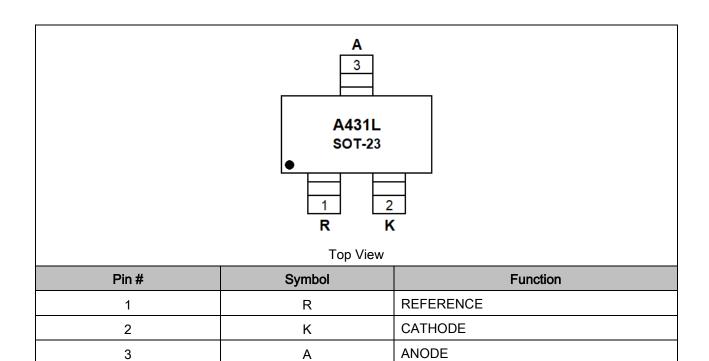
- Shunt Regulator
- High-Current Shunt Regulator
- Precision Current Limiter

TYPICAL APPLICATION



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PIN DESCRIPTION



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ABSOLUTE MAXIMUM RATINGS

Operating temperature rangeapplies unless otherwise specified

V _{KA} , Cathode Voltage	18V
I _{KA} , Cathode Current Range (Continuous)	100mA
IREF, Reference Input Current Range	6μΑ
P _D , Power Dissipation	350mW
R _{0JA} , Thermal Resistance from Junction to Ambient	357°C/W
T _{OPR} , Operating Temperature	0°C~+70°C
T _J , Junction Temperature	150°C
T _{STG} , Storage Temperature	-65°C~ +150°C

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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ELECTRICAL CHARACTERISTICS

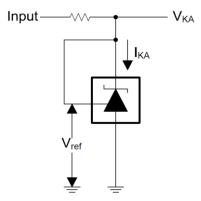
T_A=25°C, unless otherwise specified

Parameter	Symbol	Conditions		Min	Тур.	Max	Unit
Reference Input Voltage(Fig 1)	VREF	\/ -\/	0.5%	1.2338	1.24	1.2464	V
		V _{KA} =V _{REF} , I _{KA} =10mA	1%	1.2276	1.24	1.2524	V
			1.5%	1.2214	1.24	1.2586	V
Deviation of Reference Voltage	Δ	V _{KA} =V _{REF} , I _{KA} =10mA		-	-	16	mV
Over Full Temperature Range(Fig 1)	V _{REF(DEV)}	0°C≤T _A ≤70°C					
Ratio of Change in Reference Input	ΔV_{REF}	I _{KA} =10mA		-	-	2.4	mV/V
Voltage to the change in Cathode	ΔVREF/	ΔV _{KA} =1.25V~1					
Voltage (Fig 2)	ΔVKA	ΔVKA-1.25V~15V					
Deviation of Reference Input	$\Delta I_{REF}/$	I _{KA} =10mA, R ₁ =	-	·	0.6	μΑ	
Voltage Over Temperature	ΔΙΡΕΓ/						
Range(Fig 2)	Δ1	R ₂ =∞, 0°C≤T _A ≤70°C					
Minimum Cathode Current for	lee a mo	V _{KA} =V _{REF}		ı	ı	0.1	mA
Regulation(Fig 1)	I _{KA(MIN)}						
Off-state Cathode Current(Fig 3)	l _{off}	V _{KA} =15V, V _{REF} =0		-	-	0.5	μΑ
		V _{KA} =V _{REF} ,					
Dynamic Impedance	ZKA	I _{KA} =0.1~20mA,	-	_	0.5	Ω	
		f≤1.0kHz					

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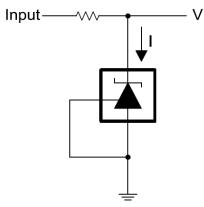
TEST CIRCUIT

Figure 1. Test Circuit for V_{KA}=V_{ref}



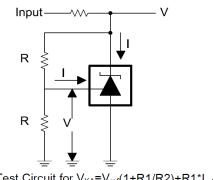
Test Circuit for V_{KA}=V_{ref}

Figure 3. Test Circuit for Ioff



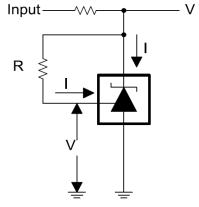
Test Circuit for Ioff

Figure 2. Test Circuit for V_{KA}=V_{ref}(1+R1/R2)+R1*I_{ref}



Test Circuit for $V_{KA}=V_{ref}(1+R1/R2)+R1*I_{ref}$

Figure 4. Test Circuit for Iref



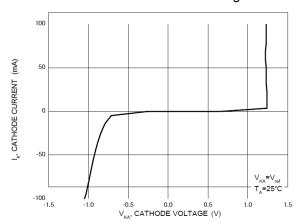
Test Circuit for Iref

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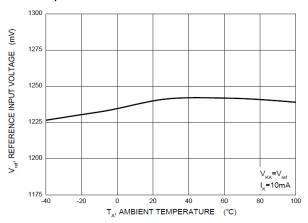
TYPICAL PERFORMANCE CHARACTERISTICS

1. Cathode Current vs. Cathode Voltage



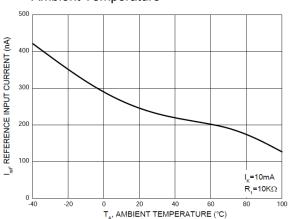
3. Reference Input Voltage vs. Ambient



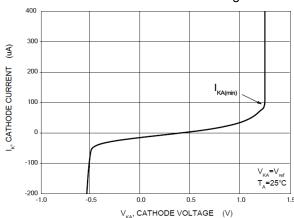


5. Reference Input Current vs.

Ambient Temperature

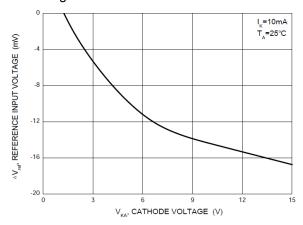


2. Cathode Current vs. Cathode Voltage



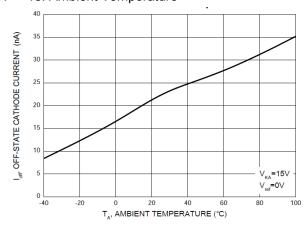
4. Change in Reference Input Voltage vs. Cathode

Voltage



6. Off-State Cathode Current

7. vs. Ambient Temperature



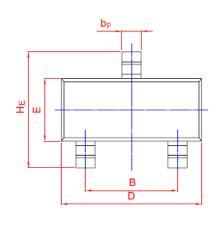
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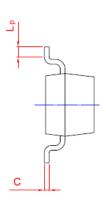


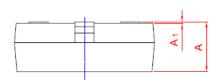
PACKAGE INFORMATION

Dimension in SOT-23 Package (Unit: mm) Plastic surface mounted package; 3 leads









UNIT	Α	В	b _p	С	D	Е	HE	A 1	Lp
mm	1.40	2.04	0.50	0.19	3.10	1.65	3.00	0.100	0.50
	0.95	1.78	0.35	0.08	2.70	1.20	2.20	0.013	0.20

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