Product Features: Small surface mountable, Solid state, Faster time to trip than standard SMD devices,

Lower resistance than standard SMD devices

Operation Current: 0.10A~1.00A Maximum Voltage: 6V_{DC}~24V_{DC} Temperature Range: -40°C to 85°C Applications: All high-density boards



Electrical Characteristics (23°C)

Part	Hold	Trip	Rated	Max	Typical	Max Time	e to Trip	Resis	stance
Number	Current	Current	Voltage	Current	Power	Current	Time	R _{MIN}	R1 _{MAX}
Number	I _H , A	lτ, Α	V _{MAX} , V _{DC}	I _{MAX} , A	Pd, W	Α	Sec	Ohms	Ohms
F0805L010-15	0.10	0.30	15	100	0.5	0.50	1.50	0.700	6.000
F0805L010-24	0.10	0.30	24	100	0.5	0.50	1.50	0.700	6.000
F0805L020-09	0.20	0.50	9	100	0.5	8.00	0.02	0.400	3.500
F0805L035-06	0.35	0.75	6	100	0.5	8.00	0.10	0.250	1.200
F0805L050-06	0.50	1.00	6	100	0.5	8.00	0.10	0.150	0.850
F0805L050-09	0.50	1.00	9	100	0.5	8.00	0.10	0.150	0.850
F0805L075-06	0.75	1.50	6	100	0.6	8.00	0.20	0.090	0.350
F0805L100-06	1.00	1.95	6	100	0.6	8.00	0.30	0.060	0.210
F0805L110-06	1.10	2.20	6	1.00	0.6	8.00	0.20	0.05	0.200

 $I_{\text{H}}\text{=}\text{Hold}$ current-maximum current at which the device will not trip at 23°C $\,$ still air.

 $I_{\text{T}}\text{=}\text{Trip}$ current-minimum current at which the device will always trip at 23°C $\,$ still air.

V_{MAX}=Maximum voltage device can withstand without damage at it rated current.(I_{MAX})

I_{MAX}= Maximum fault current device can withstand without damage at rated voltage (V_{MAX}).

 $\label{power_dissipated} \mbox{Pd=Typical power dissipated-type amount of power dissipated by the device when in the tripped state in 23 ^{\circ}C \ \ still \ air \ environment.}$

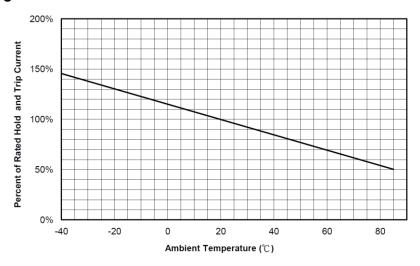
R_{MIN}=Minimum device resistance at 23°C prior to tripping.

 $R1_{MAX}$ =Maximum device resistance at 23°C measured 1 hour after tripping or reflow soldering of 260°C for 20 seconds.

Termination pad characteristics

Termination pad materials: Pure Tin

Thermal Derating Curve





Typical Time-To-Trip at 23°C

A = F0805L010-15 /

F0805L010-24

B = F0805L020-09

C = F0805L035-06

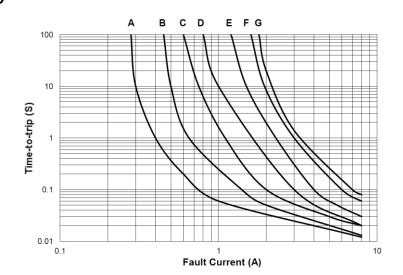
D = F0805L050-06 /

F0805L050-09

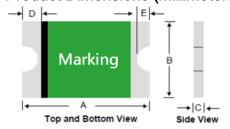
E = F0805L075-06

F = F0805L100-06

G = F0805L110-06



Product Dimensions (Millimeters)



Part	Α		В		С		D		E	
Number	Min	Max								
F0805L010-15	2.00	2.30	1.20	1.50	0.30	1.00	0.20	0.60	0.10	0.45
F0805L010-24	2.00	2.30	1.20	1.50	0.30	1.00	0.20	0.60	0.10	0.45
F0805L020-09	2.00	2.30	1.20	1.50	0.30	1.00	0.20	0.60	0.10	0.45
F0805L035-06	2.00	2.30	1.20	1.50	0.25	0.75	0.20	0.60	0.10	0.45
F0805L050-06	2.00	2.30	1.20	1.50	0.55	1.25	0.20	0.60	0.10	0.45
F0805L050-09	2.00	2.30	1.20	1.50	0.55	1.25	0.20	0.60	0.10	0.45
F0805L075-06	2.00	2.30	1.20	1.50	0.55	1.25	0.20	0.60	0.10	0.45
F0805L100-06	2.00	2.30	1.20	1.50	0.75	1.80	0.20	0.60	0.10	0.45
F0805L110-06	2.00	2.30	1.20	1.50	0.75	1.80	0.20	0.60	0.10	0.45

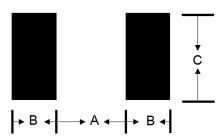
Material Specification

Terminal pad material: Pure Tin

Soldering characteristics: Meets EIA specification RS 186-9E, ANSI/J-std-002 Category 3

Pad Layouts, Solder Reflow and Rework Recommendations

The dimension in the table below provide the recommended pad layout for each F0805L device



Pad dimensions (millimeters)						
Device	Α	В	С			
	Nominal	Nominal	Nominal			
F0805L	1.20	1.00	1.50			

Profile Feature	Pb-Free Assembly		
Average Ramp-Up Rate (Tsmax to Tp)	3°C/second max.		
Preheat:			
Temperature Min (Tsmin)	150°C		
Temperature Max Tsmax)	200°C		
Time (tsmin to tsmax)	60~180 seconds		
Time maintained above:			
Temperature(T _L)	217℃		
Time (t∟)	60~150 seconds		
Peak/Classification Temperature(Tp):	260°C		
Time within 5°C of actual Peak:			
Temperature (tp)	20~40 seconds		
Ramp-Down Rate:	6°C/second max.		
Time 25°C to Peak Temperature:	8 minutes max.		

Note 1: All temperatures refer to of the package, measured on the package body surface.

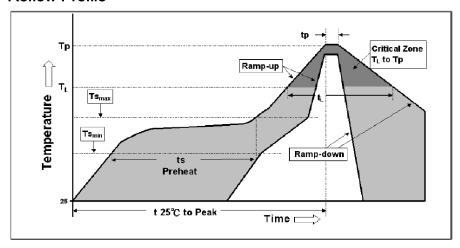
Solder reflow

- Due to "Lead Free" nature, Temperature and Dwelling time for the soldering zone is higher than those for Regular. This may cause damage to other components.
- Recommended max paste thickness is 0.25mm. (Nominal)
- 2. Devices can be cleaned using standard methods and aqueous solvent.
- 3. Rework use standard industry practices.
- 4. Storage Environment: < 30°C / 60%RH

Caution:

- 1. If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.
- 2. Devices are not designed to be wave soldered to the bottom side of the board.

Reflow Profile



NOTE: Specification subject to change without notice.

Warning:

- Operation beyond the specified maximum ratings or improper use may result in damage and possible electrical arcing and/or flame.
- PPTC device are intended for occasional overcurrent protection. Application for repeated overcurrent condition and/or prolonged trip are not anticipated.
- Avoid contact of PPTC device with chemical solvent. Prolonged contact will damage the device performance.