DESCRIPTION

The ESD5D5.0C is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.

The ESD5D5.0C is available in SOD-523 Package

FEATURES

- Small Body Outline Dimensions
- Low Body Height
- Peak Power up to 150 Watts @ 8 x 20 μs Pulse
- Low Leakage current
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000−4−2 Level 4 ESD Protection
- IEC61000−4−4 Level 4 EFT Protection
- Available in SOD-523 Package

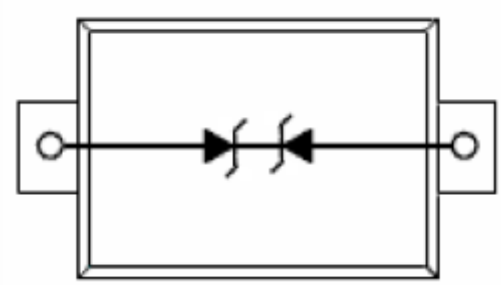
ORDERING INFORMATION

Package Type	Part Number
SOD-523	ESD5D5.0C
Note	SPQ: 3,000pcs/Reel
AiT provides all RoHS Compliant Products	

APPLICATIONS

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

PIN DESCRIPTION



ABSOLUTE MAXIMUM RATINGS

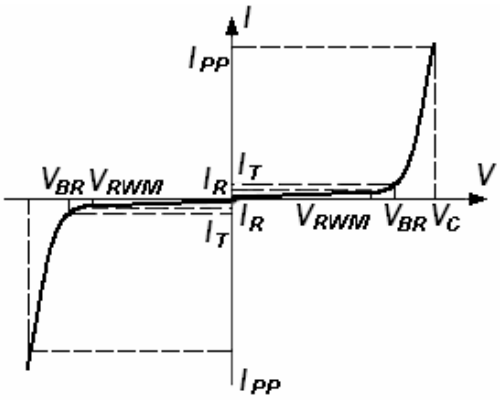
T_{AMB}=25°C

P_{PP}, Peak Pulse Power (t_P = 8/20us)		150W
T_L, Maximum lead temperature for soldering during 10s		260ºC
T_{STG}, Storage Temperature Range		-55ºC ~155ºC
T_{OP}, Operating Temperature Range		-40ºC ~125ºC
T_J, Maximum junction temperature		150ºC
IEC61000-4-2 (ESD)	air discharge	±15kV
	contact discharge	±8kV
IEC61000-4-4 (EFT)		40A
ESD Voltage	Per Human Body Model	16kV

Stresses above may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated in the Electrical Characteristics are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL PARAMETER

Symbol	Parameter
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Maximum Reverse Leakage Current @ V_{RWM}
I_T	Test Current
V_{BR}	Breakdown Voltage @ I_T



ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. V_F = 0.9V at I_F = 10mA

Part Number	V_{RWM} (V)	I_R(μA) @ V_{RWM}	V_{BR}(V) @ I_T NOTE1		I_T (mA)	V_C (V) @ I_{PP}=5A*	V_C (V) @ Max I_{PP}*	I_{PP} (A)*	P_{PK} (W)*	C (pF)
	Max	Max	Min	Max		Typ	Max	Max	Max	Typ
ESD5D5.0C	5.0	1	5.6	7.8	1.0	11.6	18.6	9.4	174	15

NOTE1: V_{BR} is measured with a pluse test current I_T at an ambient temperature of 25°C.
NOTE2:*Surge current waveform per Figure 1.

TYPICAL CHARACTERISTICS

Figure1. Pulse Waveform

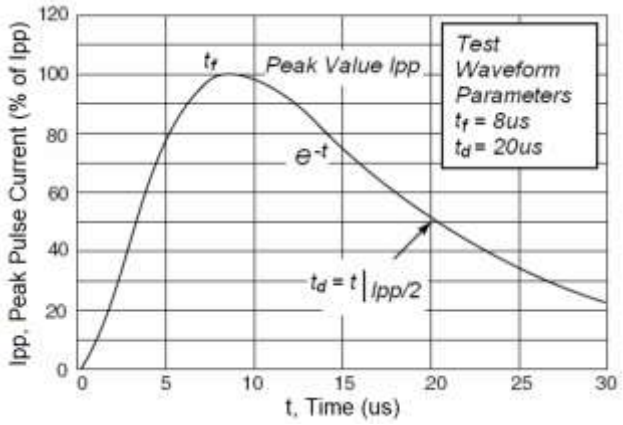
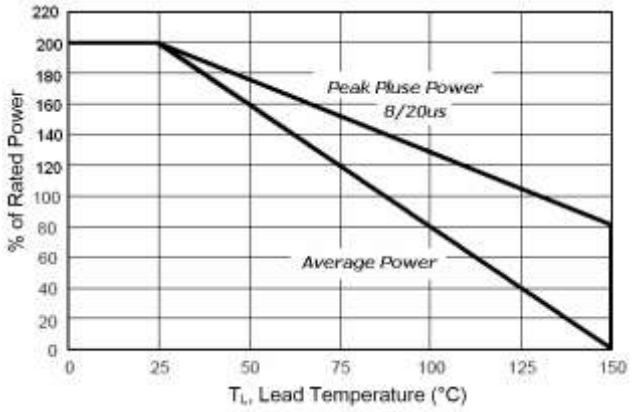


Figure 2. Power Derating



APPLICATION NOTE

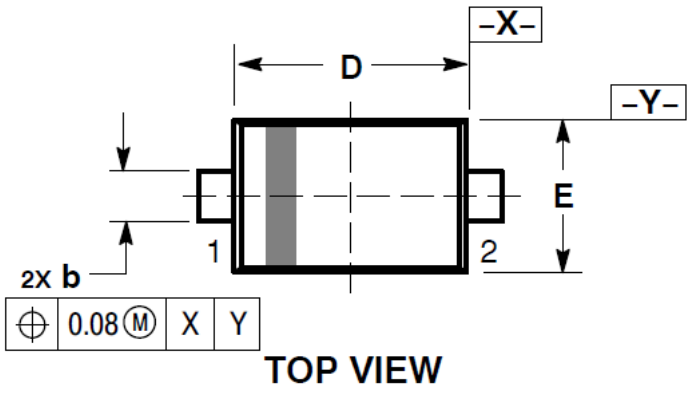
Electrostatic discharge (ESD) is a major cause of failure in electronic systems. Transient Voltage Suppressors (TVS) are an ideal choice for ESD protection. They are capable of clamping the incoming transient to a low enough level such that damage to the protected semiconductor is prevented.

Surface mount TVS offers the best choice for minimal lead inductance. They serve as parallel protection elements, connected between the signal lines to ground. As the transient rises above the operating voltage of the device, the TVS becomes a low impedance path diverting the transient current to ground. The ESD5D5.0C is the ideal board evel protection of ESD sensitive semiconductor components.

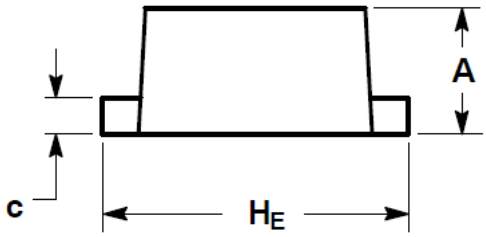
The tiny SOD-523 package allows design flexibility in the design of high density boards where the space saving is at a premium. This enables to shorten the routing and contributes to hardening against ESD.

PACKAGE INFORMATION

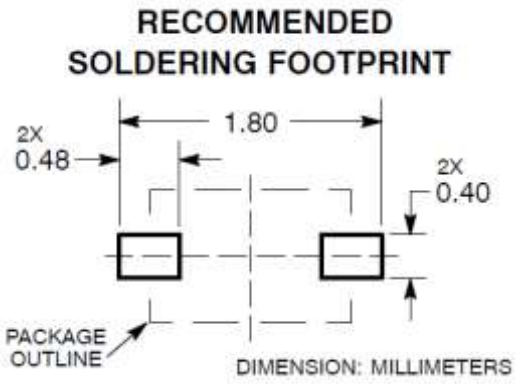
Dimension in SOD-523 Package (Unit: mm)



TOP VIEW



SIDE VIEW



DIM	MIN	MAX
A	0.50	0.70
b	0.25	0.35
c	0.07	0.20
D	1.10	1.30
E	0.70	0.90
H_E	1.50	1.70
L	0.30REF	
L2	0.15	0.25

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