



## DESCRIPTION

AM3483 is available in a SOT-26 package.

## ORDERING INFORMATION

Package Type	Part Number	
SOT-26	E6	AM3483E6R
		AM3483E6VR
Note	V: Halogen free Package R: Tape & Reel	
AiT provides all RoHS products Suffix " V " means Halogen free Package		

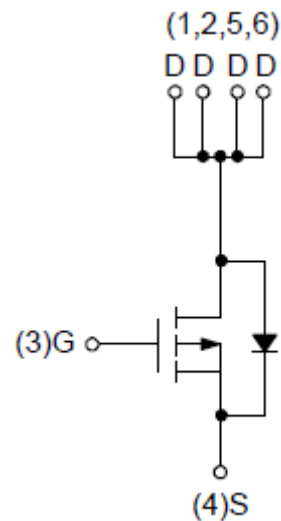
## FEATURES

- -30V/-7.5V,  
 $R_{DS(ON)} = 30m\Omega(\text{max.}) @ V_{GS} = -10V$   
 $R_{DS(ON)} = 45m\Omega(\text{max.}) @ V_{GS} = -4.5V$
- Super High Dense Cell Design
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- Available in a SOT-26 package.

## APPLICATION

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

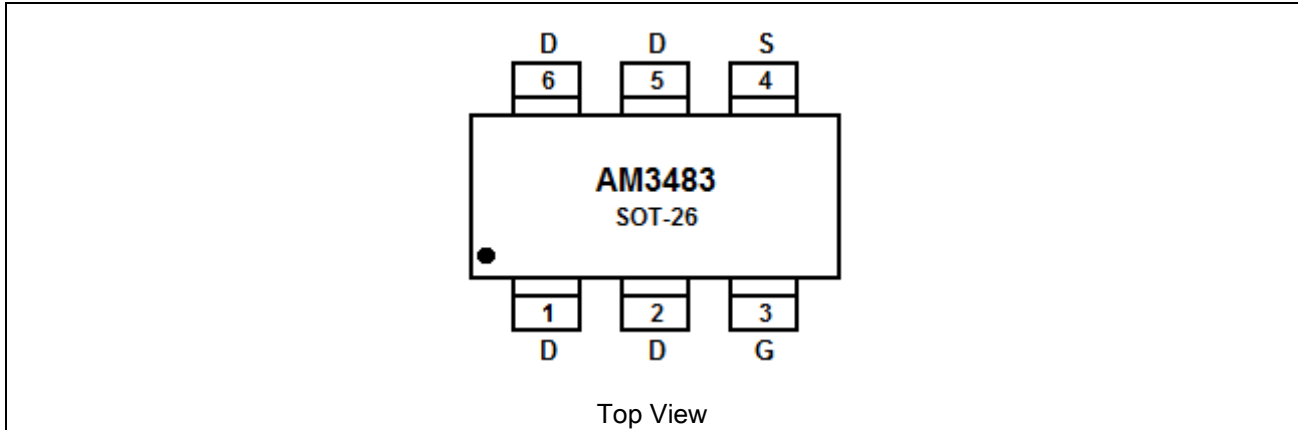
## PIN DESCRIPTION



P-Channel MOSFET



## PIN DESCRIPTION



Pin #	Symbol	Function
1,2,5,6	D	Drain
3	G	Gate
4	S	Source



## ABSOLUTE MAXIMUM RATINGS

T<sub>A</sub> = 25°C, unless otherwise noted

V <sub>DSS</sub> , Drain-Source Voltage		-30V
V <sub>GSS</sub> , Gate-Source Voltage		±20V
I <sub>D</sub> <sup>NOTE1</sup> , Continuous Drain Current (V <sub>GS</sub> =-10V)	T <sub>A</sub> =25°C	-7.5A
	T <sub>A</sub> =70°C	-6A
I <sub>DM</sub> <sup>NOTE1</sup> , 300µs Pulsed Drain Current (V <sub>GS</sub> =-10V)		-30A
I <sub>S</sub> <sup>NOTE1</sup> , Diode Continuous Forward Current		-2A
I <sub>AS</sub> <sup>NOTE2</sup> , Avalanche Current, Single pulse (L=0.3mH)		-16A
E <sub>AS</sub> <sup>NOTE2</sup> , Avalanche Energy, Single pulse (L=0.3mH)		38mJ
T <sub>J</sub> , Maximum Junction Temperature		150°C
T <sub>STG</sub> , Storage Temperature Range		-55°C~150°C
P <sub>D</sub> <sup>NOTE1</sup> , Maximum Power Dissipation	T <sub>A</sub> =25°C	2.5W
	T <sub>A</sub> =70°C	1.6W
R <sub>θJA</sub> <sup>NOTE1</sup> , Thermal Resistance-Junction to Ambient	t ≤ 10s	50°C/W
	Steady state	80°C/W

Stress beyond above listed "Absolute Maximum Ratings" may lead permanent damage to the device. These are stress ratings only and operations of the device at these or any other conditions beyond those indicated in the operational sections of the specifications are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

NOTE1: Surface Mounted on 1in<sup>2</sup> pad area, t ≤ 10sec

NOTE2: UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature T<sub>J</sub>=25°C).



## ELECTRICAL CHARACTERISTICS

T<sub>A</sub> = 25°C, unless Otherwise Noted

Parameter	Symbol	Conditions	Min	Typ.	Max	Units
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>DS</sub> =-250μA	-30	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V	-	-	-1	μA
		T <sub>J</sub> =85°C			-30	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =-250μA	-1.3	-1.8	-2.3	V
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
Drain-Source On-state Resistance	R <sub>DS(ON)</sub> NOTE3	V <sub>GS</sub> =-10V, I <sub>DS</sub> =-7.5A	-	24	30	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>DS</sub> =-4A	-	33	45	
<b>Diode Characteristics</b>						
Diode Forward Voltage	V <sub>SD</sub> NOTE3	I <sub>SD</sub> =-2A, V <sub>GS</sub> =0V	-	-0.7	-1	V
Reverse Recovery Time	t <sub>rr</sub> NOTE4	I <sub>SD</sub> =-7.5A,	-	19	-	ns
Reverse Recovery Charge	Q <sub>rr</sub> NOTE4	di <sub>SD</sub> /dt=100A/μs	-	9	-	nC
<b>Dynamic Characteristics</b> NOTE4						
Gate Resistance	R <sub>G</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	-	7	-	Ω
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, Frequency=1.0MHz	-	840	-	pF
Output Capacitance	C <sub>OSS</sub>		-	150	-	
Reverse Transfer Capacitance	C <sub>RSS</sub>		-	110	-	
Turn-on Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> =-15V, R <sub>L</sub> =15Ω I <sub>DS</sub> =-1A, V <sub>GEN</sub> =-10V, R <sub>G</sub> =6Ω	-	7.5	-	ns
Turn-on Rise Time	t <sub>R</sub>		-	8	-	
Turn-off Delay Time	t <sub>D(OFF)</sub>		-	37	-	
Turn-off Fall Time	t <sub>F</sub>		-	16	-	
<b>Gate Charge Characteristics</b> NOTE4						
Total Gate Charge	Q <sub>G</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V, I <sub>DS</sub> =-7.5A	-	18	-	nC
Gate-Source Charge	Q <sub>GS</sub>		-	3	-	
Gate-Drain Charge	Q <sub>GD</sub>		-	4	-	

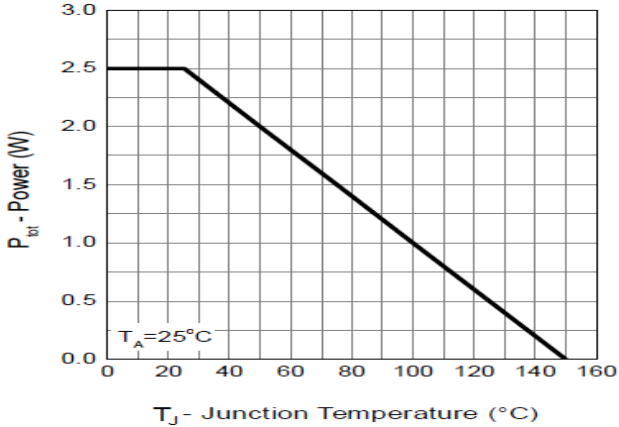
NOTE3: Pulse test; pulse width≤300μs, duty cycle≤2%.

NOTE4: Guaranteed by design, not subject to production testing.

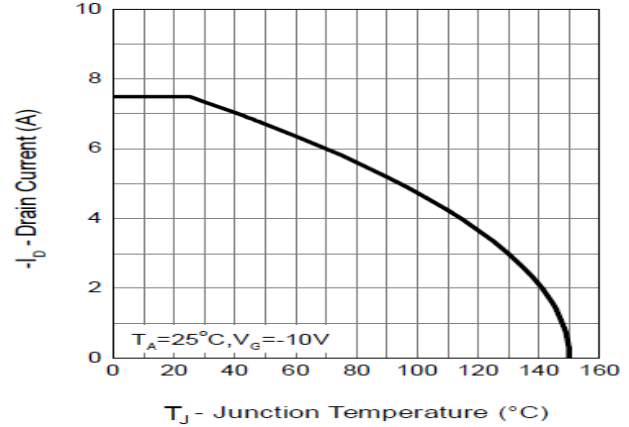


## TYPICAL CHARACTERISTICS

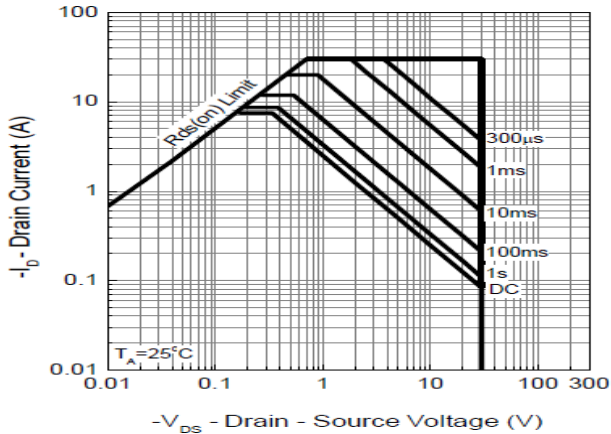
### 1. Power Dissipation



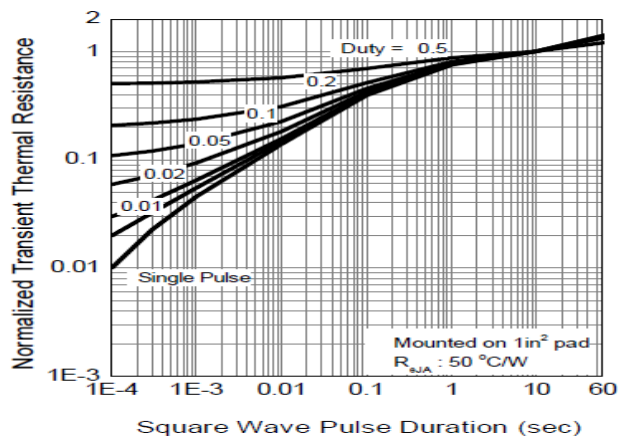
### 2. Drain Current



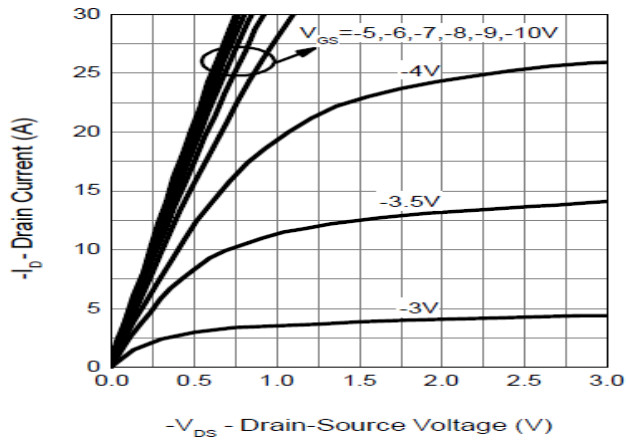
### 3. Safe Operation Area



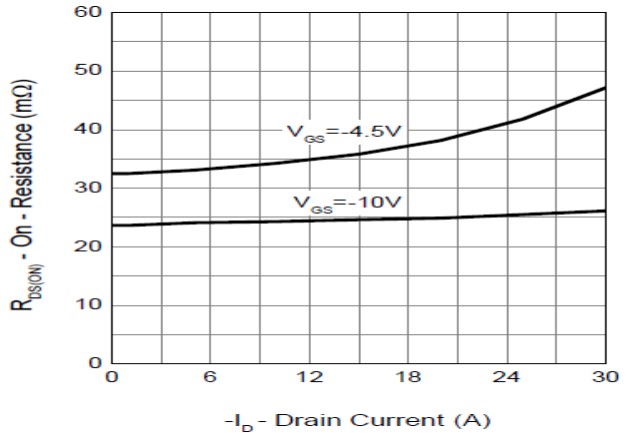
### 4. Thermal Transient Impedance



### 5. Output Characteristics

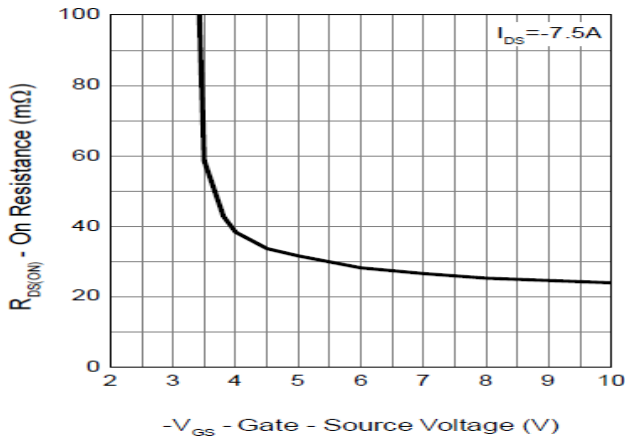


### 6. Drain-Source On Resistance

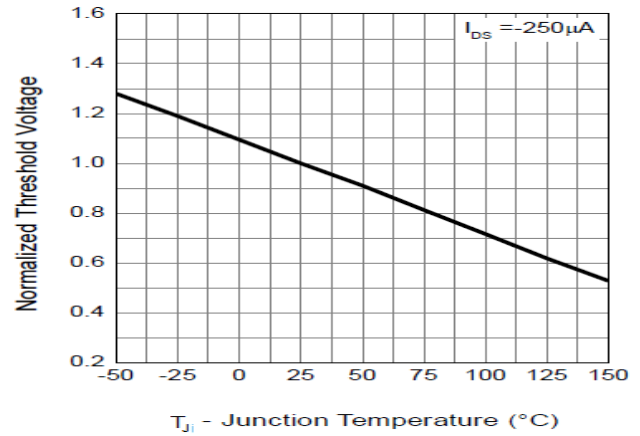




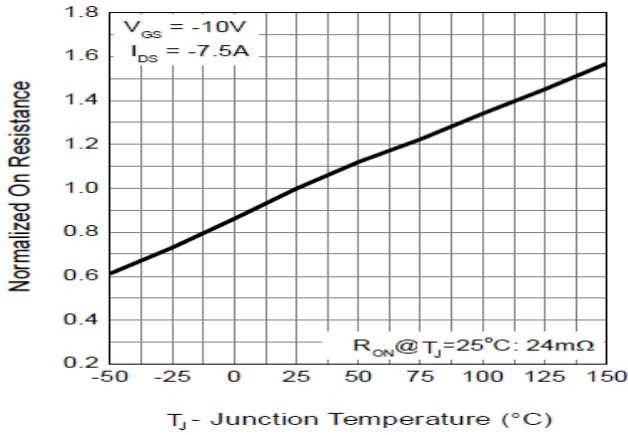
7. Gate-Source On Resistance



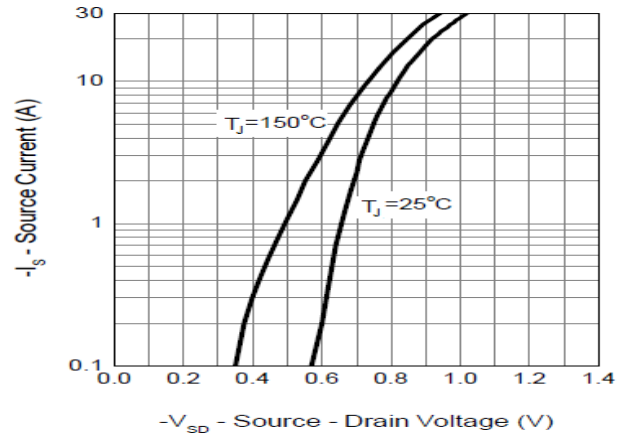
8. Gate Threshold Voltage



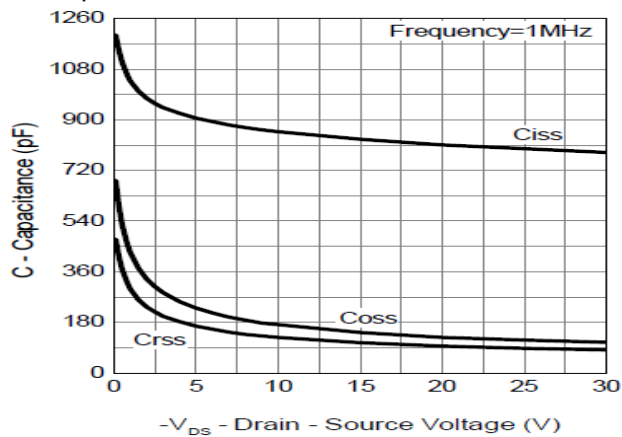
9. Drain-Source On Resistance



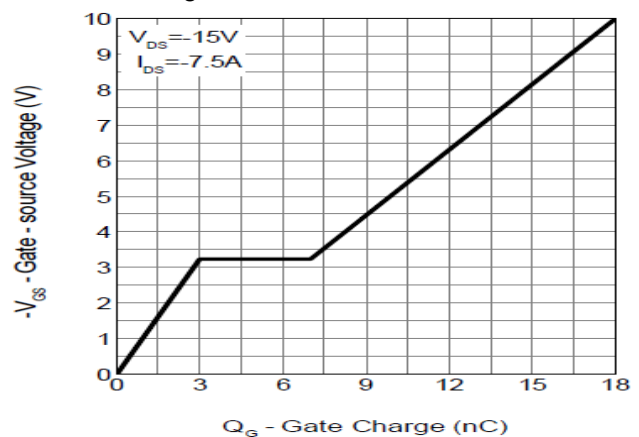
10. Source-Drain Diode Forward



11. Capacitance



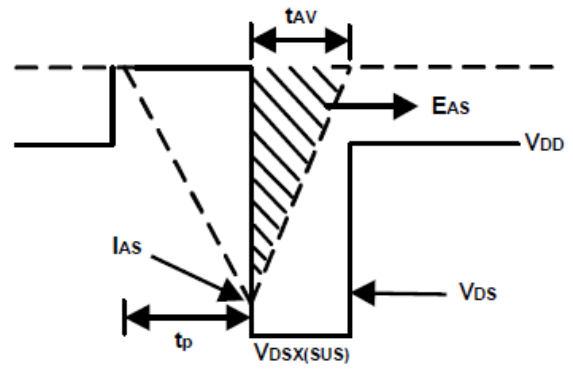
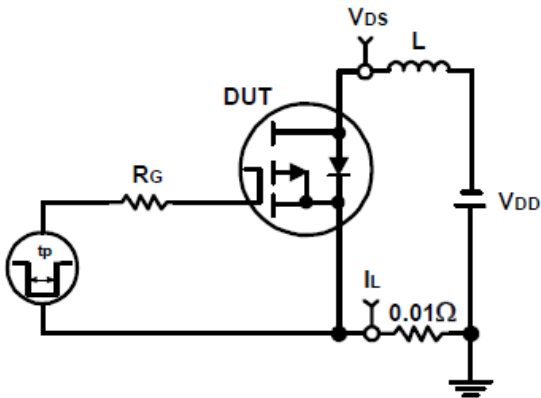
12. Gate Charge



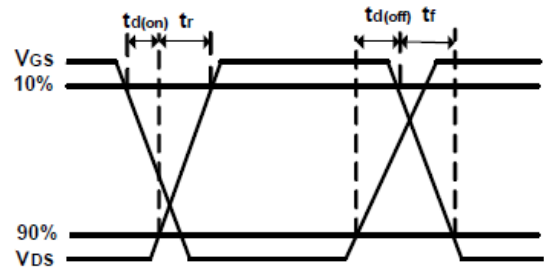
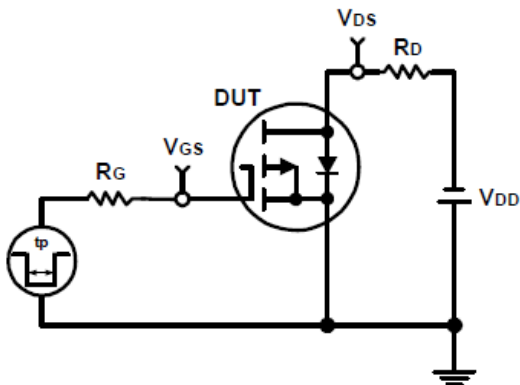


## DETAILED INFORMATION

### Avalanche Test Circuit and Waveforms



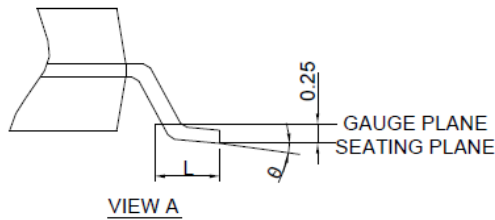
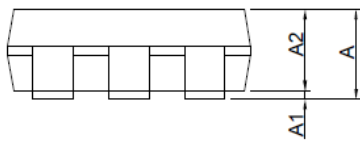
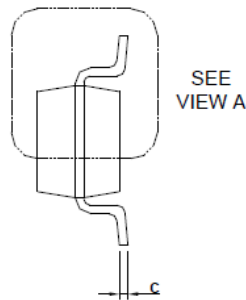
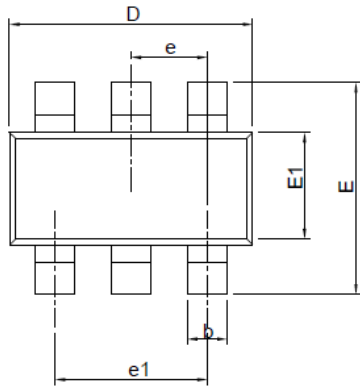
### Switching Time Test Circuit and Waveforms



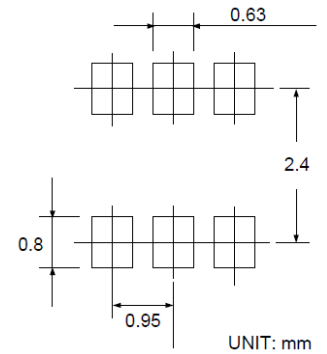


**PACKAGE INFORMATION**

Dimension in SOT-26 Package (Unit: mm)



**RECOMMENDED LAND PATTERN**



SYMBOL	MIN	MAX
A	-	1.250
A1	0.000	0.150
A2	0.900	1.300
b	0.300	0.500
c	0.080	0.220
D	2.700	3.100
E	1.400	1.800
E1	2.600	3.000
e	0.950(BSC)	
e1	1.900(BSC)	
L	0.300	0.600
θ	0°	8°





## IMPORTANT NOTICE

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